

Electronic Acknowledgement Receipt

EFS ID:	5234832
Application Number:	10575758
International Application Number:	
Confirmation Number:	5126
Title of Invention:	Printed Circuit Board Design Instruction Support Method and Device
First Named Inventor/Applicant Name:	Hiromichi Inaishi
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Filer Authorized By:	Paul C. Lewis
Attorney Docket Number:	1794-0182PUS1
Receipt Date:	28-APR-2009
Filing Date:	05-MAR-2007
Time Stamp:	15:49:19
Application Type:	U.S. National Stage under 35 USC 371

Payment information:

Submitted with Payment	yes
Payment Type	Deposit Account
Payment was successfully received in RAM	\$ 1170
RAM confirmation Number	2153
Deposit Account	022448
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1		20090428RCE.PDF	177779 62fb8fad4c7311bf5f5498ba6745eadc96498aed	yes	3

Multipart Description/PDF files in .zip description

Document Description	Start	End
Request for Continued Examination (RCE)	1	1
Extension of Time	2	2
Fee Worksheet (PTO-875)	3	3

Warnings:**Information:**

2	Fee Worksheet (PTO-875)	fee-info.pdf	32018 a32a60d92bd8a1db6daa8fba45e540ad9309ea35	no	2
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Warnings:**Information:**

Total Files Size (in bytes):	209797
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

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